

Features

- Operating voltage: 5V~12V
- Standby current: 100μA (Typ.)
- On-chip regulator
- Adjustable output duration
- CDS input
- 40 second warm-up
- ON/AUTO/OFF selectable by MODE pin
- Override function
- Auto-reset if the ZC signal disappears over 3 seconds
- 16-pin DIP package

Applications

- PIR light controllers
- Motion detectors
- Alarm systems
- Auto door bells

General Description

The HT761x is a CMOS LSI chip designed for use in automatic PIR lamp control. It can operate with a 2-wire configuration for triac applications or with a 3-wire configuration for relay applications. The chip is equipped with operational amplifiers, a comparator, timer, a zero crossing detector, control circuit, a voltage regulator, a system oscillator, and an output timing oscillator.

Its PIR sensor detects infrared power variations induced by the motion of a human body and transforms it to a

voltage variation. If the PIR output voltage variation conforms to the criteria (refer to the functional description), the lamp is turned on with an adjustable duration.

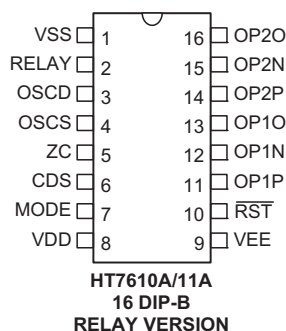
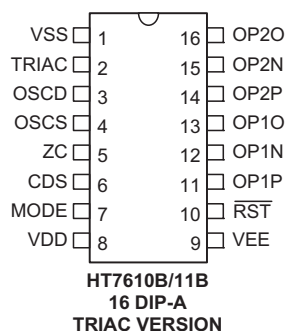
The HT761x offers three operating modes (ON, AUTO, OFF) which can be set through the MODE pin. While the chip is working in the AUTO mode the user can override it and switch to the TEST mode, or manual ON mode, or return to the AUTO mode by switching the power switch.

Selection Table

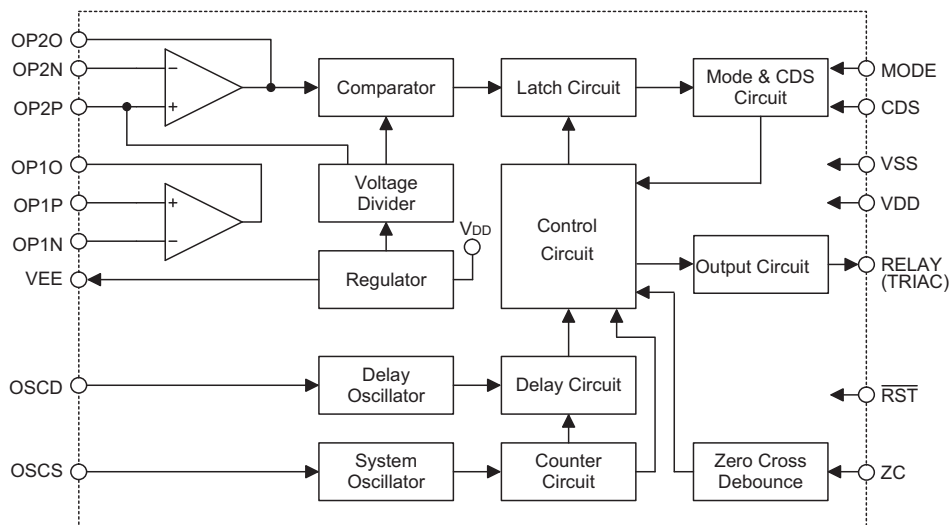
Part No.	ZC Off/On for Override	Flash on Mde Auto-change	Override ON Duration	Comparator Window	Effective Trigger Width
HT7610A HT7610B	2 times	Flash	8 hrs	$\frac{1}{16} (V_{DD}-V_{EE})$	>24ms
HT7611A HT7611B	1 time	No flash	8 hrs	$\frac{1}{16} (V_{DD}-V_{EE})$	>24ms

Note: Part numbers suffixed with A are for Relay application while those suffixed with B are for Triac application.

Pin Assignment



Block Diagram



Pin Description

Pin Name	I/O	Internal Connection	Description
VSS	—	—	Negative power supply, ground
RELAY	O	CMOS	RELAY drive output through an external NPN transistor, active high.
TRIAC	O	CMOS	TRIAC drive output The output is a pulse output when active.
OSCD	I/O	PMOS IN NMOS OUT	Output timing oscillator I/O It is connected to an external RC to adjust output duration.
OSCS	I/O	PMOS IN NMOS OUT	System oscillator I/O OSCS is connected to an external RC to set the system frequency. The system frequency is at 16kHz for normal application.
ZC	I	CMOS	Input for AC zero crossing detection
CDS	I	CMOS	CDS is connected to a CDS voltage divider for daytime/night auto-detection. Low input to this pin can disable the PIR input. CDS a Schmitt Trigger input with 5-second input debounce time.
MODE	I	CMOS	Operating mode selection input: VDD: Output is always ON VSS: Output is always OFF Open: Auto detection
VDD	—	—	Positive power supply
VEE	O	NMOS	Regulated voltage output The output voltage is -4V with respect to VDD.
RST	I	Pull-High	Chip reset input, active low
OP1P	I	PMOS	Noninverting input of OP1
OP1N	I	PMOS	Inverting input of OP1
OP1O	O	NMOS	Output of OP1
OP2P	I	PMOS	Noninverting input of OP2
OP2N	I	PMOS	Inverting input of OP2
OP2O	O	NMOS	Output of OP2

Absolute Maximum Ratings

Supply Voltage	-0.3V to +13V	Storage Temperature	-50°C to 125°C
Input Voltage	$V_{SS}-0.3V$ to $V_{DD}+0.3V$	Operating Temperature	-25°C to 70°C
Zero Crossing Current	Max. 300 μ A		

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

Symbol	Parameter	Test Condition		Min.	Typ.	Max.	Unit
		V_{DD}	Condition				
V_{DD}	Operating Voltage	—	—	5	9	12	V
V_{EE}	Regulator Output Voltage	12V	$V_{DD}-V_{EE}$	3.5	4.0	4.5	V
I_{DD}	Operating Current	12V	No load, OSC on	—	100	350	μ A
I_{OH1}	OUTPUT Source Current (RELAY, TRIAC)	12V	$V_{OH}=10.8V$	-6	12	—	mA
I_{OL1}	OUTPUT Sink Current (RELAY, TRIAC)	12V	$V_{OL}=1.2V$	40	80	—	mA
I_{OL2}	VEE Sink Current	12V	$V_{DD}-V_{EE}=4V$	—	—	—	mA
V_{IH}	"H" Input Voltage	—	—	$0.8V_{DD}$	—	—	V
V_{IL}	"L" Input Voltage	—	—	—	—	$0.2V_{DD}$	V
V_{TH1}	CDS "H" Transfer Voltage	12V	—	6.4	8.0	9.6	V
V_{TL1}	CDS "L" Transfer Voltage	12V	—	3.7	4.7	5.6	V
V_{TH2}	ZC "H" Transfer Voltage	12V	—	4.7	6.7	8.7	V
V_{TL2}	ZC "L" Transfer Voltage	12V	—	1.3	1.8	2.3	V
V_{OS}	OP Amp Input Offset Voltage	12V	No load	—	10	35	mV
f_{SYS}	System Oscillator Frequency	12V	$R_{OSCS}=560k\Omega$ $C_{OSCS}=100pF$	12.8	16	19.2	KHz
f_d	Delay Oscillator Frequency	12V	$R_{OSCD}=560k\Omega$ $C_{OSCD}=100pF$	12.8	16	19.2	KHz
A_{VO}	OP Amp Open Loop Gain	12V	No load	60	80	—	dB

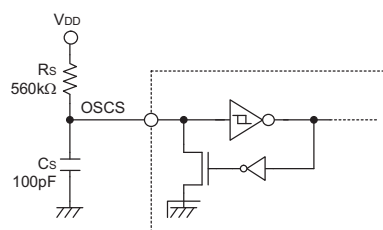
Functional Description

VEE

VEE supplies power to the analog front end circuit with a normally stabilized voltage of -4V with respect to VDD.

OSCS

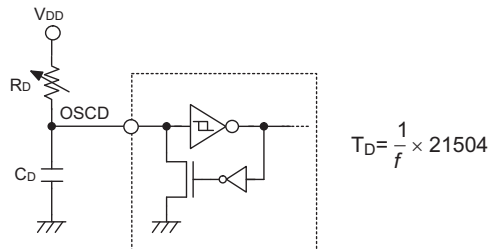
OSCS is a system oscillator input pin. When it is connected to an external RC, a system frequency of 16kHz can be generated.



System Oscillator

OSCD

OSCD is an output timing oscillator input pin. It is connected to an external RC to obtain the desired output turn-on duration. Variable output turn-on durations can be achieved by selecting various values of RC or using a variable resistor.



Output Timing Oscillator

RELAY (TRIAC)

RELAY is an output pin set as a RELAY driving (active high) output for the HT7610A/11A, or as a TRIAC driving (active low) output for the HT7610B/11B.

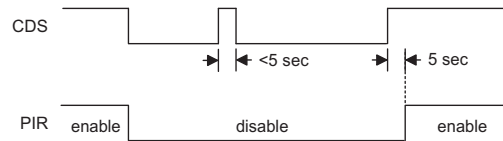
The output active duration is controlled by the OSCD oscillating period.

	HT7610A/11A	HT7610B/11B
OUTPUT	RELAY	TRIAC

CDS

CDS is a CMOS Schmitt Trigger input structure. It is used to distinguish between day time and night time. When the input voltage of CDS is high the PIR input is enabled. On the other hand, when CDS is low the PIR input is disabled. The input disable to enable debounce time is 5 seconds. Connect this pin to VDD when this function is not used. The CDS input is ignored when the output is active.

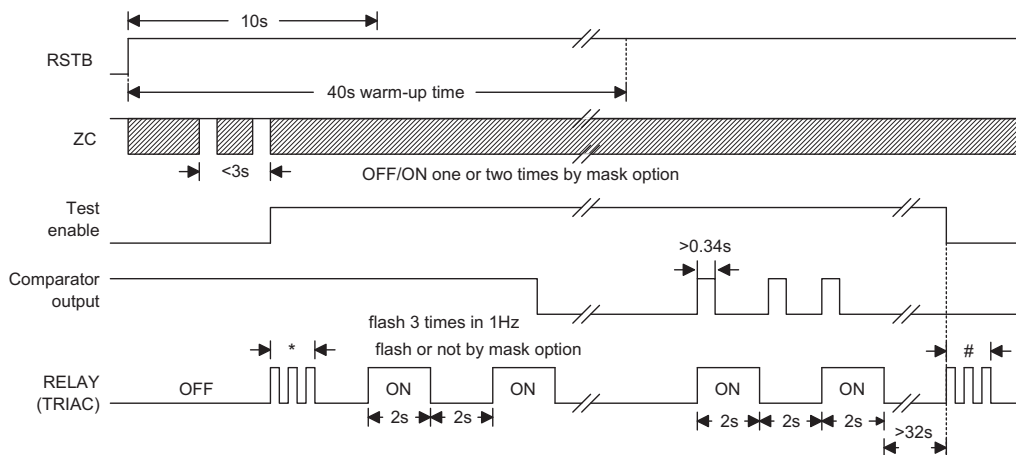
CDS	Status	PIR
LOW	Day Time	Disabled
HIGH	Night	Enabled



MODE

MODE is a tristate input pin used to select the operating mode.

MODE Status	Operating Mode	Description
VDD	ON	Output is always ON: RELAY outputs high for relay driving. TRIAC pulse train output is synchronized by ZC for triac driving.
VSS	OFF	Output is always OFF: RELAY outputs low for relay driving. TRIAC outputs high for triac driving.
Open	AUTO	Outputs remain in the off state until activated by a valid PIR input trigger signal. When working in the AUTO mode, the chip allows override control by switching the ZC signal.



* : flash 3 times at a 1Hz rate
flash or not by mask option
: flash 3 times at a 2Hz rate
flash or not by mask option

ZC

ZC is a CMOS input structure. It receives AC line frequency and generates zero crossing pulses to synchronize the triac driver. By effective ZC signal switching (switch OFF/ON 1 or 2 times within 3 seconds by mask option), the chip provides the following additional functions:

- **Test mode control**
Within 10 seconds after power-on, effective ZC switching will force the chip to enter the test mode. During the test mode, the outputs will be active for a duration of 2 seconds each time a valid PIR trigger signal is received. If a time interval exceeds 32 seconds without a valid trigger input, the chip will automatically enter the AUTO mode.
- **Override control**
When the chip is working in an AUTO mode (MODE=open), the output is activated by a valid PIR trigger signal and the output active duration is controlled by an OSCD oscillating period. The lamp can be switched always to "ON" from the AUTO mode by either switching the MODE pin to VDD or switching the ZC signal by an OFF/ON operation of the power switch (OFF/ON once or twice within 3 seconds by mask option). The term "override" refers to the change of operating mode by switching the power switch. The chip can be toggled from ON to AUTO by an override operation. If the chip is overridden to ON and there is no further override operation, it will automatically return to AUTO after an internal preset ON time duration has elapsed. This override ON time duration is 8 hours.
The chip provides a mask option to determine the output flash times (3 times) when changing the operating mode. It will flash 3 times at a 1Hz rate each time the chip changes from an AUTO mode to another mode or flash 3 times at a 2Hz rate when returning to the AUTO mode. But if the AUTO mode is changed by switching the MODE switch it will not flash.

RST

RST is used to reset the chip. It is internal pull-high and active low.

The use of C_{RST} can extend the power-on initial time. If the RST pin is an open circuit (without C_{RST}), the initial time is the default (40 secs).

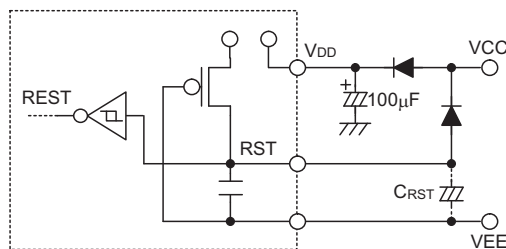


Fig.1 RST Application Example

Power-on Initial

The PIR signal amplifier requires a warm up period after power-on. The input should be disabled during this period.

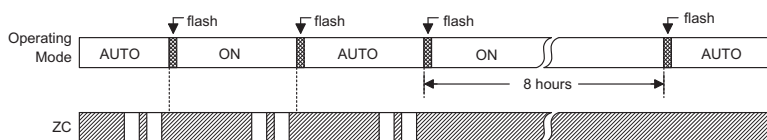
In the AUTO mode within the first 10 seconds of power-on initialization, the chip allows override control to enter the test mode. After 40 seconds of the initial time the chip allows override control between ON and AUTO. It will remain in the warm up period if the total initial time has not elapsed after returning to AUTO.

In case that the ZC signal disappears for more than 3 seconds, the chip will restart the initialization operation. However, the restart initial time is always 40 seconds and cannot be extended by adding C_{RST} to the RST pin as shown in the Fig.1.

Mask Options

The HT761x offers mask options to select the output flash (3 times) when changing the operating mode. The chip will flash 3 times at a 1Hz rate each time it changes from AUTO to another mode and flash 3 times at a 2Hz rate when it returns to the AUTO mode. However the chip will not flash if the mode is changed by switching the MODE switch.

- Options for effective override: Once or twice Off/On operation of power switch within 3 seconds. Once for HT7611A/HT7611B and twice for HT7610A/HT7610B.
- Options for output flash to indicate effective override operation. Flash for HT7610A/HT7610B and no flash for HT7611A/HT7611B.



ZC Override Timing

PIR Amplifier

Consult the diagram below for details on the PIR front end amplifier.

In the Fig.2 below there are 2 op-amps with different applications. OP1 can be used independently as a first stage inverting or non-inverting amplifier for the PIR.

As the output of OP2 is directly connected to the input of the comparator, it is used as a second stage amplifying device. The non-inverting input of OP2 is connected to the comparator's window center point and can be used to check this voltage and to provide a bias voltage that is equal to the center point voltage of the comparator. In Fig.2 the window level of the comparator is set to $\frac{1}{16}$

$(V_{DD}-V_{EE})$.

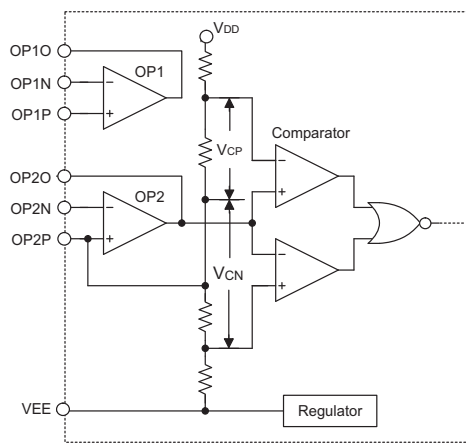


Fig.2 PIR Amplifier

The preset voltage of $V_{DD}-V_{EE}$ is 4V. The default values of V_{CP} and V_{CN} are therefore 0.25V, $(\frac{4}{16} V)$.

Second Stage Amplifier

Usually the second stage PIR amplifier is a simple capacitively coupled inverting amplifier with a low pass configuration. The noninverting input terminal is biased to the center point of the comparator window and the output of the second stage amplifier is directly coupled to the comparator center point.

In Fig.3 OP2P is directly connected to the comparator window center, and with the C3 filter it can act as the bias for OP2. For this configuration $A_V = \frac{R_2}{R_1}$, low cutoff

frequency $f_L = \frac{1}{2\pi R_1 C_1}$, high cutoff frequency

$f_H = \frac{1}{2\pi R_2 C_2}$. By changing the value of R2 the sensitivity can be varied. C1 and C3 should be of low leakage

types to prevent the DC operating point from changing due to current leakage.

Each op-amp current consumption is approximately 5μA with the op-amps and comparator's working voltage all provided by the regulator.

Consult the following diagrams for typical PIR front end circuit.

First Stage of PIR Amplifier

Fig.4 shows a typical first stage amplifier. C2 and R2 form a simple low pass filter with cut off frequency at 7Hz. The low frequency response is governed by R1 and C1 with cut-off frequency at 0.33Hz.

$$A_V = \frac{(R_1 + R_2)}{R_1}$$

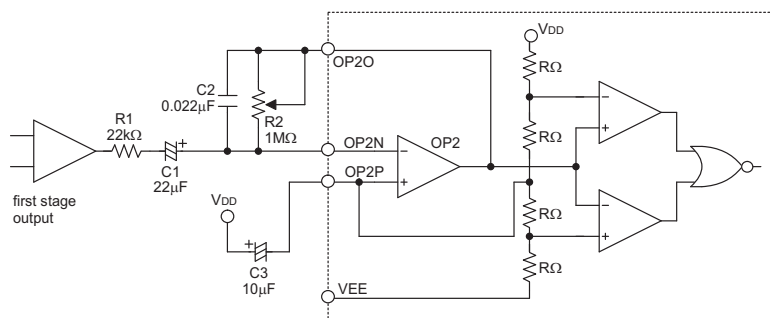


Fig.3 Typical Second Stage Amplifier

Fig.4 and Fig.5 are similar but in Fig.5 the input signal of amplifier is taken from the drain of the PIR. This has higher gain than that in Fig.4. Since OP1 is a PMOS input VD, it has to be greater than 1.2V for adequate operation.

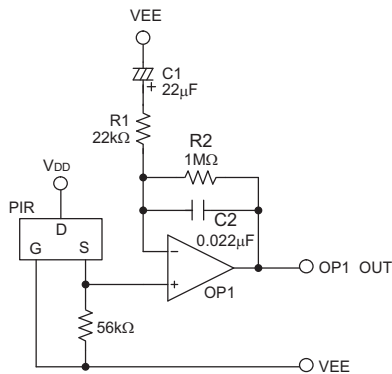


Fig. 4 Typical First-Stage PIR

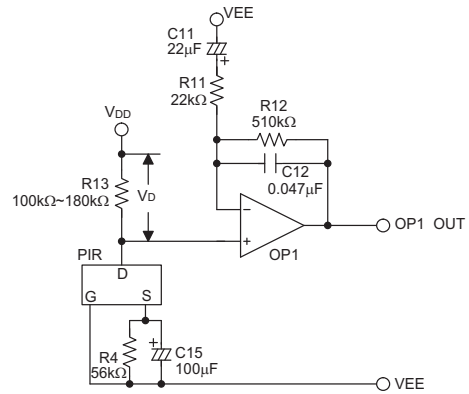
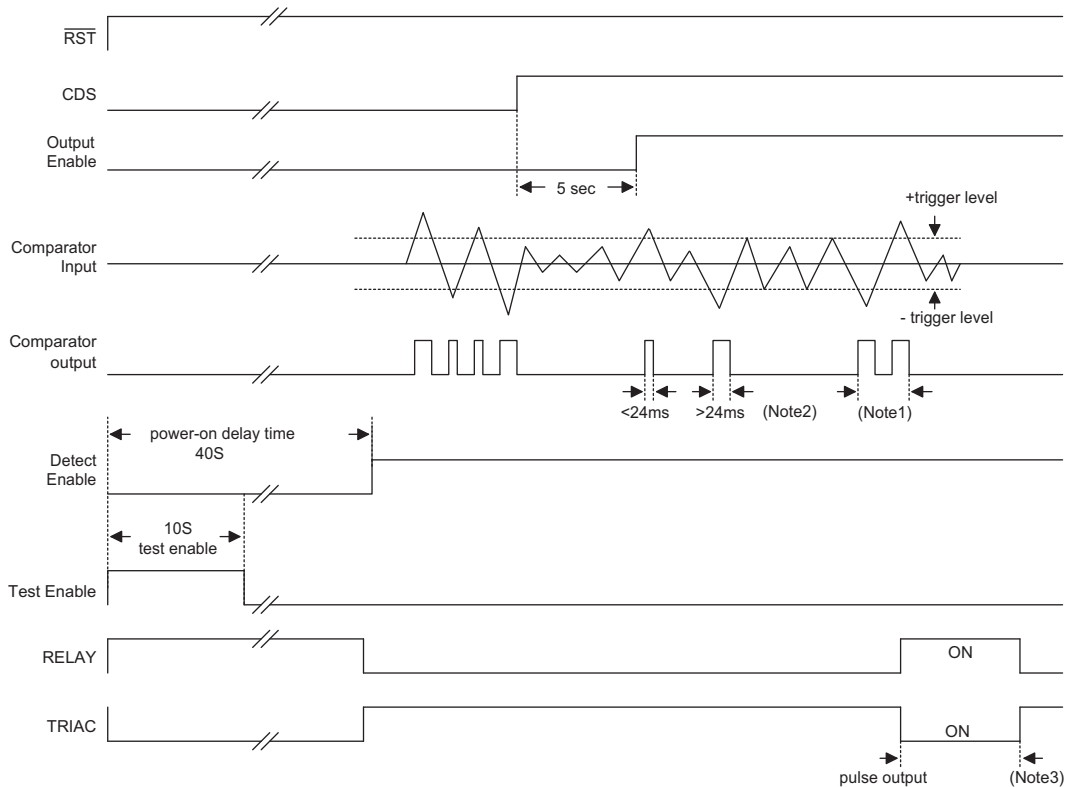


Fig.5 High Gain First Stage

Timing Diagram



Note: The output is activated if the trigger signal conforms to the following criteria:

- More than 3 triggers within 2 seconds
- A trigger signal sustain duration ≥ 0.34 secs
- 2 trigger signals within 2 secs with one of the trigger signal sustain ≥ 0.16 secs.

The effective comparator output width is selected to be 24ms.

The output duration is set by an external RC that is connected to the OSCD pin.

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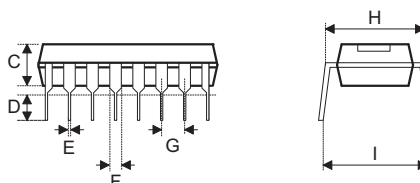
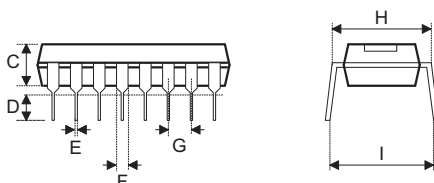
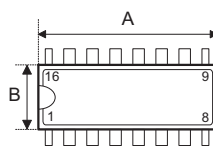
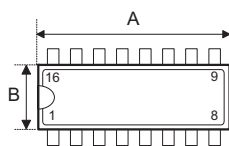
Change the value of "****" to 0.15 μ F/ 600V for AC 220V application.

Package Information

Note that the package information provided here is for consultation purposes only. As this information may be updated at regular intervals users are reminded to consult the [Holtek website](#) for the latest version of the package information.

Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

- [Further Package Information](#) (include Outline Dimensions, Product Tape and Reel Specifications)
- [Packing Materials Information](#)
- [Carton information](#)
- [PB FREE Products](#)
- [Green Packages Products](#)

16-pin DIP (300mil) Outline Dimensions

Fig1. Full Lead Packages
Fig2. 1/2 Lead Packages

- MS-001d (see fig1)

Symbol	Dimensions in inch		
	Min.	Nom.	Max.
A	0.780	—	0.880
B	0.240	—	0.280
C	0.115	—	0.195
D	0.115	—	0.150
E	0.014	—	0.022
F	0.045	—	0.070
G	—	0.100	—
H	0.300	—	0.325
I	—	0.430	—

Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	19.81	—	22.35
B	6.10	—	7.11
C	2.92	—	4.95
D	2.92	—	3.81
E	0.36	—	0.56
F	1.14	—	1.78
G	—	2.54	—
H	7.62	—	8.26
I	—	10.92	—

- MS-001d (see fig2)

Symbol	Dimensions in inch		
	Min.	Nom.	Max.
A	0.735	—	0.775
B	0.240	—	0.280
C	0.115	—	0.195
D	0.115	—	0.150
E	0.014	—	0.022
F	0.045	—	0.070
G	—	0.100	—
H	0.300	—	0.325
I	—	0.430	—

Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	18.67	—	19.69
B	6.10	—	7.11
C	2.92	—	4.95
D	2.92	—	3.81
E	0.36	—	0.56
F	1.14	—	1.78
G	—	2.54	—
H	7.62	—	8.26
I	—	10.92	—

- MO-095a (see fig2)

Symbol	Dimensions in inch		
	Min.	Nom.	Max.
A	0.745	—	0.785
B	0.275	—	0.295
C	0.120	—	0.150
D	0.110	—	0.150
E	0.014	—	0.022
F	0.045	—	0.060
G	—	0.100	—
H	0.300	—	0.325
I	—	0.430	—

Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	18.92	—	19.94
B	6.99	—	7.49
C	3.05	—	3.81
D	2.79	—	3.81
E	0.36	—	0.56
F	1.14	—	1.52
G	—	2.54	—
H	7.62	—	8.26
I	—	10.92	—

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